# DUE DATES: FRIDAY, OCTOBER 26. OPTIONAL REVISION DUE MONDAY, NOVEMBER 19.

No extensions will be given except in case of illness or other emergency.

General Guidelines

- You may work in groups of up to four. Turn in just one paper for the whole group. Each member of the group will receive the same grade. If you would like to work in a group but cannot find a group to join, please let me know no later than Friday, October 5.
- 2. If you choose to turn in a revised version of your report, 90% of your grade will be based on the final version and 10% on the first version.
- 3. This is a major assignment, and you should plan to do some work on it every day, starting today. DO NOT WAIT UNTIL THE LAST MINUTE.
- 4. The paper you turn in must be your group's work. I reserve the right to ask any or all of you for a verbal explanation of your solution.
- 5. I strongly encourage you to see me regularly as you work on the project to discuss your progress. If you receive any assistance from anyone other than me, of if you use our textbook or any other outside source, you must cite the source in your paper. Remember guideline number 4.

## What Should You Hand In?

After you have completely solved all parts of the assigned problem, you should write a report explaining your solution. The report should contain a mixture of text, equations, and graphs, and possibly tables and diagrams. Your text should be grammatically correct, and you should use proper punctuation and spelling. If you do not type the paper, please write legibly. Your paper should contain an introduction explaining the problem and should clearly explain each step of your solution. Assume the reader knows something about differential equations but has not read the project description below. Please note that you will be graded on the quality of your presentation as well as on the mathematical content. You may find the following checklist helpful. It was adapted from a checklist developed by Dr. Annalisa Crannell of Franklin and Marshall College. Does this paper

- 1. clearly restate the problem to be solved?
- 2. clearly label diagrams, tables, and graphs?
- 3. define all variables used?
- 4. provide a paragraph explaining how the problem will be approached?
- 5. explain how each formula is derived or give a reference indicating where it can be found?
- 6. give acknowledgment where it is due?

In this paper,

- 7. are the spelling, grammar, and punctuation correct?
- 8. is the mathematics correct?
- 9. did you answer all the questions that were asked?

1. Derivation of IVP in Part 1 (10 points)	
2a. Derivation of IVP in Part 2 (10 points)	
2b. Proof that $v = q$ and $v_r = dq/dt$ (10 points)	
3a. Solution of IVP $(1)$ for sinusoidal input $(10 \text{ points})$	
3b. Differentiation of solution to 3a (5 points)	
3ci. Identification of transient and steady-state terms (5 points)	
3cii. Derivation of amplitude formula (10 points)	
3ciii. Plot of amplitude vs. frequency and explanation of significance (10 points)	
3di. Identification of transient and steady-state terms (5 points)	
3dii. Derivation of amplitude formula (5 points)	
3diii. Plot of amplitude vs. frequency and explanation of significance (10 points)	
4. Report (introduction, clarity and completeness of presentation, grammar). (10 points)	
Total.	

### **Project Description**

#### **Background Information**

The purpose of this project is to develop and analyze a mathematical model of an RC circuit. (See the diagram below.) The circuit consists of a voltage source, a resistor of resistance R, and a capacitor of capacitance C. At some instant, which we take to be time 0, the switch is closed, completing the circuit.

We can consider this circuit to be a system to which the input is the applied voltage E and from which the output is either the voltage measured across the capacitor,  $V_C$ , or the voltage measured across the resistor,  $V_R$ . You will study the response of this system to a sinusoidal applied voltage E, and you will show how the circuit can act as a filter.



In the remainder of this project description, the following notation will be used.

T	time (seconds)
Q	charge on the capacitor at time $T$ (coulombs)
Ι	current in the circuit at time $T$ (amperes)
E	applied voltage (volts)
$E_0$	maximum value of $ E $ (volts)
$V_C$	voltage across the capacitor (volts)
$V_R$	voltage across the resistor (volts)
C	capacitance of the capacitor (farads)
R	resistance of the resistor (ohms)
t = T/(RC)	dimensionless time
$q = Q/(E_0C)$	dimensionless charge on the capacitor
$\epsilon = E/E_0$	dimensionless applied voltage
$v_c = V_C / E_0$	dimensionless voltage across the capacitor
$v_r = V_R / E_0$	dimensionless voltage across the resistor

- 1. Formulation of the Model. Use the following information to formulate an initial value problem (differential equation and initial condition) modeling this circuit. Take T to be the independent variable and Q to be the dependent variable.
  - Kirchoff's Second Law says that the sum of the voltage drops across the resistor and capacitor equals the applied voltage E.
  - The voltage drop across the resistor equals *RI*.
  - The voltage drop across the capacitor equals Q/C.
  - I = dQ/dT.
  - The charge on the capacitor is initially 0.
- 2. Nondimensionalization. A useful technique for analyzing mathematical models is to introduce dimensionless variables. This technique has two advantages: it usually reduces the number of parameters, and it helps makes clear which combination(s) of parameters determine the behavior of the solution. Let t = T/RC, let  $q = Q/E_0C$ , let  $\epsilon = E/E_0$ , let  $v_c = V_C/E_0$ , and let  $v_r = V_R/E_0$ , where  $E_0$  denotes the maximum value of |E| (assumed to be nonzero). Since RC has units of time, t has no units; it is a pure number. Similarly, q,  $\epsilon$ , and v are dimensionless quantities. A value of t = 1 corresponds to a time T of one "time constant" RC.
  - a. Show that the initial value problem you derived in part 1 can be rewritten in terms of dimensionless variables as follows:

$$\frac{dq}{dt} + q = \epsilon, \quad q(0) = 0. \tag{1}$$

b. Show that  $v_c = q$  and that  $v_r = dq/dt$ .

### 3. How the RC Circuit Acts as a Filter.

- a. Solve the IVP (1) for  $\epsilon = \cos(\omega t)$  (which corresponds to an input voltage  $E = E_0 \cos(\omega T/RC)$ ).
- b. Find the derivative q' of your solution to part a.
- c. Low Pass Filter In this part of the project, we consider the output of the RC circuit system to be the voltage measured across the capacitor:  $V_C = Q/C$ . The dimensionless output is therefore  $v_c = q$ .
  - i. Show that the response q from part a contains a transient term  $q_{tr}$  that approaches 0 as  $t \to \infty$  and a steady-state term  $q_{ss}$  that does not approach 0.
  - ii. Find the amplitude  $A_c$  of the steady-state solution  $q_{ss}$ . (See section 3.4 of the textbook. There the amplitude is denoted C, but we are already using the letter C to denote capacitance. Your expression for  $A_c$  will contain  $\omega$ .)
  - iii. Plot  $A_c$  vs.  $\omega$  on a loglog plot for  $0.01 \le \omega \le 1000$ . Explain how this graph shows that the RC circuit acts as a low-pass filter, filtering out high-frequency signals.
- d. High Pass Filter In this part of the project, we consider the output of the RC circuit system to be the voltage measured across the resistor:  $V_R = RI$ . The dimensionless output is therefore  $v_r = q'$ .
  - i. Show that the response q' from part b contains a transient term  $q'_{tr}$  that approaches 0 as  $t \to \infty$  and a steady-state term  $q'_{ss}$  that does not approach 0.
  - ii. Find the amplitude  $A_r$  of the steady-state solution  $q'_{ss}$ .
  - iii. Plot  $A_r$  vs.  $\omega$  on a loglog plot for  $0.01 \le \omega \le 1000$ . Explain how this graph shows that the RC circuit acts as a high-pass filter, filtering out low-frequency signals.