

Chapter 3

- 28. Register operand addressing mode
Immediate operand addressing mode
Memory operand addressing modes
- 32. (a) PA= 0B200H
(b) PA= 0B100H
(c) PA= 0B700H
(d) PA= 0B600H
(e) PA= 0B900H

Chapter 4

4. 3 bytes

- 16. -F CS: 100 105 11 (ENTER)
-F CS: 106 10B 22 (ENTER)
-F CS: 10C 111 33 (ENTER)
-F CS: 112 117 44 (ENTER)
-F CS: 118 11D 55 (ENTER)
-E CS: 105 (ENTER)
CS: 0105 XX.FF (ENTER)
-E CS: 113 (ENTER)
CS: 0113 XX.FF (ENTER)
-D CS: 100 11D (ENTER)
-S CS: 100 11D FF (ENTER)

Chapter 5

- 10. AL is loaded from the physical address $10000H + 0100H + 0010H = 10110H$
- 16. ADC EDX, 111FH
- 26. (a) (DS:300H)= 0AH
(b) (DX)= A00AH
(c) (DS:210H)= FFFFH
(d) (DS: 220H)= F5H
(e) (AX)= AA55H
(f) (DS:300H)= 55H
(g) (DS: 210H)= 55H, (DS: 211H)= 55H